



IFW 2128

BUCKET NO. 99-B-186  
CLIENT NO.: STMI01-99186  
Customer No. 30425

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Vidyabhusan Gupta  
Serial No.: 09/591,621  
Filed: June 9, 2000  
For: SYSTEM AND METHOD FOR DESIGNING AND  
OPTIMIZING THE MEMORY OF AN EMBEDDED  
PROCESSING SYSTEM  
Group No.: 2128  
Examiner: Herng Der Day

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

Sir:

**CERTIFICATE OF MAILING BY FIRST CLASS MAIL**

The undersigned hereby certifies that the following documents:

1. Amendment and Response Under 37 C.F.R. §1.111; and
2. Postcard Receipt

relating to the above application, were deposited as "First Class Mail", with the United States Postal Service, addressed to: MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 17, 2006.

Date: April 17, 2006

Date: April 17, 2006

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**AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111**

In response to the Office Action dated January 18, 2006, please amend the above-identified patent application as follows.